

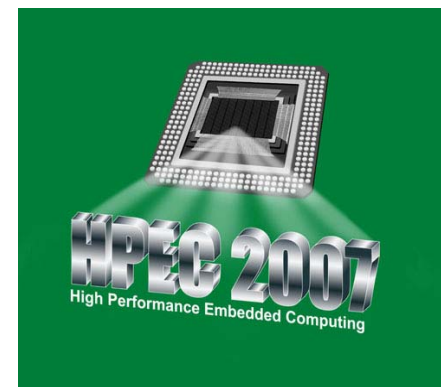


Session 3: Multicore Hardware Challenges

Robert Bond / MIT Lincoln Laboratory

HPEC Conference

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MIT Lincoln Laboratory